

## High-speed UARTs and Bridge ICs

Advanced serial-interface solutions





Fastest baud rate: 20 Mbps SC16C85xS



Deepest FIFO: 256 Bytes SC28L202



Smallest footprint: 12.25 mm<sup>2</sup> SC16C85xLIET

NXP UARTs offer industryleading performance, size, and FIFO depth

- ▶ Global supplier of a very broad portfolio
- ▶ High-performance solutions (speed, FIFO depth, baud rate)
- ▶ Focus on miniaturization (smaller packaging)
- ▶ In-house manufacturing and assembly
- ▶ Direct customer support: interface.support@nxp.com

### **NXP UART applications**

#### ▶ General Interface

UART controller is part of the serial data communication of a system. A UART is used to convert parallel data to serial data, and from serial data to parallel data.

### Internet access equipment

Routers, high-end modems, remote-access service, modem-access equipment for ISPs.

### ▶ Telecom and networking communication

Basestations, PABX systems, serial-to-fiber optic converters, ADSL boxes, Bluetooth based phones, hubs, switches, WLAN/802.11 GPRS.

#### ▶ Computing

PC, server, POS, storage, PDAs and internet appliances, printers, scanners, fax servers, Smart Card readers

### ▶ Appliances and terminals

Cash registers connected via serial cable to CPU, simple fixed dataentry terminals for warehouse control, card readers connected with central unit for park-house control

#### ▶ Entertainment and gaming

Midi interface on musical devices and stage equipment, data exchange and control of gambling equipment, toys, MP3, DTV, STB, projectors, digital cameras, digital LSRs, handheld games.

#### ▶ Home Security

Remote control of audio equipment from central unit, light and heating control devices in homes or offices, security sensors and surveillance devices

### ▶ Robotics

Industrial control of CNC equipment, remote sensor equipment, motor control

#### ▶ Industrial

Elevators, car-control boxes, security, medical equipment, data exchange via serial ports, lighting/gas metering, warehouse control, POS terminals, equipment control through serial connection, remote measurement, GPS navigation systems

#### **SmartPhones**

Bluetooth interface, CDMA and GSM baseband communication

## **NXP UARTs**

NXP, an established leader as a long-term supplier in UARTs, provides innovative solutions to meet the application requirements of today and tomorrow

#### **INDUSTRIAL UARTS**

NXP, the number-one supplier of industrial UARTs, offers a broad line of single- to eight-channel devices that deliver higher baud rates, superior error handling, deeper FIFOs, improved character recognition, responsive interrupt systems, and very fast host-bus cycle times. The latest implementation of the basic NXP UART architecture, the IMPACT family, offers special features – like lower operating voltage, programmable interrupt priority, and selectable Motorola/Intel bus interfaces – that lower overall cost, speed time-to-market, and improve system performance. A single IMPACT device can be used in multiple operating systems and in multiple applications, so designers can bring several systems to market using the same high-performance, simple-to-use UART.

#### **16C UARTS**

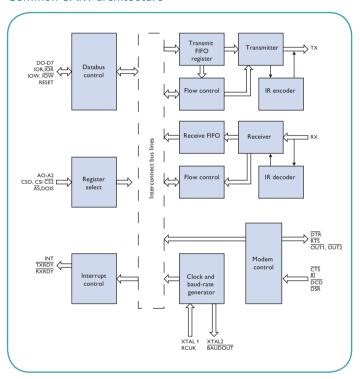
NXP's enhanced 16C UARTs are drop-in compatible with industry-standard devices and offer added features without a price premium.

The family includes CMOS-based, single- to quad-channel UARTs that support the widest supply voltage range (1.8, 2.5, 3.3 and 5 V), operate within the industrial temperature range (-40 to 85 °C), deliver baud rates up to 20 Mbps, and offer bus cycle times that are up to 20% faster than traditional devices. Many are available in tiny HVQFN and TFBGA packages that reduce PCB space by as much as 70%. The 16C UARTs are fully compatible with Linux and Windows OS drivers, thus saving precious design time and lowering overall cost.

#### SPECIAL NXP FEATURES

Both families of UARTs employ an architecture that has been carefully optimized for superior performance. Programmable channel modes increase flexibility and make diagnostics easier to run. Multi-drop support (also known as "RS-485" or " 9-bit" mode) allows half-duplex long distance communication. Hardware handshaking uses modem control signals to control the data stream, preventing FIFO overflow without interrupting the CPU.

#### Common UART architecture

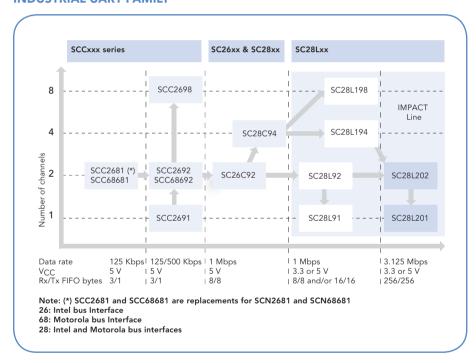


NXP UARTs are easy to design into a very broad range of applications

### **NXP** industrial UARTs

Features	Benefits
Broad line of single- to eight-channel UARTs	Variety of choices for different applications
Industrial-grade temperature range (-40 to 85 °C)	Rugged performance in extreme conditions
Power-down mode	Ideal for battery-operated systems
Extensive interrupt support	Reduced software overhead
Automatic RS485 half-duplex control	Reduced CPU overhead (multi-drop support)
Automatic out-band flow control	Avoids loss of data
Rx/Tx-independent with respect to speed & clock frequency	Rx/Tx can operate at different baud rates
Flexible and programmable I/O structure	Allows usage of I/O pins for general purposes
RTS/CTS (hardware) flow control signals	Prevent receiver overrun
3.3 and 5 V operating range (IMPACT family)	Broader range of applications
Motorola/Intel interfaces (IMPACT family)	Industry-standard compatibility and faster design-in
Deep FIFO	Very high data throughput

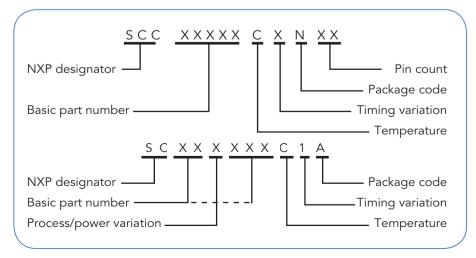
#### **INDUSTRIAL UART FAMILY**



### ADDED FEATURES OF THE IMPACT FAMILY

- Single part for multiple operating environments
- ▶ 3.3 and 5 V operation
- ► Compatible with Intel and Motorola bus interfaces
- ▶ FIFO depth up to 256 bytes
- ▶ Three bytes of character recognition
- ▶ Xon/Xoff in-band flow control
- ▶ Watchdog™ timer
- ▶ Character count mode
- ▶ Receiver time-out mode
- ▶ Programmable FIFO interrupt level
- ▶ Intelligent interrupt arbitration
- ▶ Real-time data error detection

### NAMING CONVENTIONS



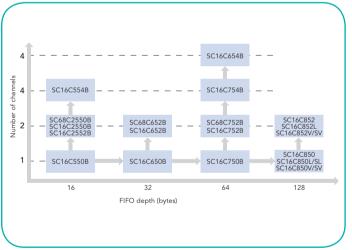
### **Industrial UART selection**

UART device	Comment	Channel	V <sub>cc</sub> (±10%)	Data rate at Vcc (Kbps)	Rx/Tx FIFO byes	Arbitrating interrupt	I/O pins	16-bit counter/timer	Rx/Tx FIFO counters	Rx/Tx FIFO INT trigger	Software flow control	Intel or Motorola databus interface	Power-down mode	Package	Part number (temp range 0 to 70 °C)	Part number (temp range -40 to 85 °C)
SCC2691	Single-channel version of SCC2692	1	5 V	125	3/1	Normal	2	1	No	3/1 level	No	Intel	Yes	DIL24 SO24 PLCC28	SCC2691AC1N24 SCC2691AC1D24 SCC2691AC1A28	SCC2691AE1N24 SCC2691AE1A28
SC28L91	Low power, single- channel version of SC28L92	1	3.3 or 5 V	1000	16/16 or 8/8	Normal Multi-level Vectored IACK/DACK	15	1	Yes	All	No	Intel or Motorola (Pin select)	Yes	PLCC44 QFP44		SC28L91A1A SC28L91A1B
SC28L201	Single-channel version of SC28L202. Enhanced, faster version of SC28L91	1	3.3 or 5 V	3125	256/ 256	Normal Multi-level IACK/DACK I2A	16	2	Yes	All	Auto	Intel or Motorola (Pin select)	Yes	TSSOP48		SC28L201A1DGG
SCC2681	CMOS version of SCN2681	2	5 V	125	3/1	Normal	15	1	No	3/1 level	No	Intel	No	DIL28 DIL40 PLCC44	SCC2681AC1N28 SCC2681AC1N40 SCC2681AC1A44	SCC2681AE1N28 SCC2681AE1N40 SCC2681AE1A44
SCC68681	CMOS version of SCN68681	2	5 V	125	3/1	Normal Vectored	14	1	No	3/1 level	No	Motorola	No	DIL40 PLCC44	SCC68681AC1N40 SCC68681AC1A44	SCC68681AE1N40 SCC68681AE1A44
SCC2681T	CMOS version of SCN2681T	2	5 V	500	3/1	Normal	15	1	No	3/1 level	No	Intel	Yes	PLCC84	SCC2681TC1A44	
SCC2692	CMOS version of SCN2681	2	5 V	125	3/1	Normal	15	1	No	3/1 level	No	Intel	Yes	DIL28 DIL40 PLCC44 QFP44	SCC2692AC1N28 SCC2692AC1N40 SCC2692AC1A44 SCC2692AC1B44	SCC2692AE1N28 SCC2692AE1N40 SCC2692AE1A44 SCC2692AE1B44
SCC68692	CMOS version of SCN68681	2	5 V	125	3/1	Normal Vectored IACK/DACK	14	1	No	3/1 level	No	Motorola	Yes	DIL40 PLCC44	SCC68692AC1N40 SCC68692AC1A44	SCC68692AE1N40 SCC68692AE1A44
SC26C92	High-speed version of SCC2692	2	5 V	1000	8/8	Normal Multi-level	15	1	Yes	All	No	Intel	Yes	DIL40 PLCC44 QFP44		SC26C92A1N SC26C92A1A SC26C92A1B
SC28L92	Low-power, faster version of SC26C92	2	3.3 or 5 V	1000	16/6 or 8/8	Normal Multi-level Vectored IACK/DACK	15	1	Yes	All	No	Intel or Motorola	Yes	PLCC44 QFP44		SC28L92A1A SC28L92A1B
SC28L202	Enhanced, faster version of SC28L92	2	3.3 or 5 V	3125	256/ 256	Normal Multi-level IACK/DACK I2A	16	2	Yes	All	Auto	Intel or Motorola (Pin select)	Yes	TSSOP56		SC28L202A1DGG
SC28C94	Enhanced quad version of SC26C92	4	5 V	1000	8/8	Normal Multi-level IACK/DACK I2A	16	2	Yes	All	No	Intel or Motorola	Yes	PLCC52		SC28C94A1A
SC28L194	Enhanced version of SC28C94	4	3.3 or 5 V	1000	16/16	Normal Multi-level IACK/DACK I2A	16	2	Yes	All	Auto	Intel or Motorola	Yes	PLCC68		SC28L194A1A SC28L194A1BE
SCC2698B	Quad version of SCC2692	8	5 V	125	3/1	Normal	32	4	No	3/1 level	No	Intel	Yes	PLCC84	SCC2698BC1A84	SCC2698BE1A84
SC28L198	Enhanced version of SCC2698B	8	3.3 or 5 V	1000	16/16	Normal Multi-level IACK/DACK I2A	32	2	Yes	All	Auto	Intel or Motorola	Yes	PLCC84		SC28L198A1A SC28L198A1BE

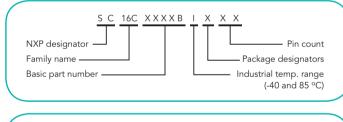
### NXP high-speed 16CxxB UARTs

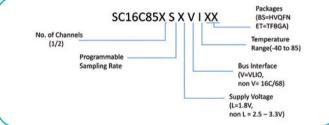
Features	Benefits
Broad line of single- to quad-channel UARTs	One-stop shopping
Widest supply voltage range (1.8, 2.5, 3.3, 5 V) at industrial temperature range (-40 to 85 °C) without price premium	Single part can be used for multiple systems, multiple operating environments Lower overall cost of ownership – can replace up to four competitor parts
Fastest devices on the market with over 20% faster bus cycle times and baud rates up to 20 Mbps	Compatible with high-speed processors Ideal for Bluetooth applications
Power-down mode	Ideal for battery-operated systems
HVQFN and BGA package options	Ideal for small, portable systems
Windows and Linux OS-compatible	Simplifies software development
Infrared (IrDA) interface	Enables wireless, short-range applications
Software readily available	Shortens design cycle
Automatic software and hardware flow control	Reduces CPU overhead and data loss
DMA mode and wide variety of FIFO depths	Increases system throughput
Drop-in compatibility with existing 16C devices	Alternative manufacturing source
16C (Intel), 68(Motorola), VLIO(Variable Latency I/O)	Wide range of processor supported
Programmable Sampling Rate	Higher baud rates
Fully automatic RS-485	Reduce processor's software overhead

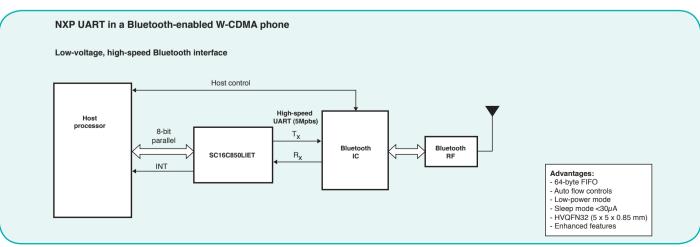
### **16C UART FAMILY**



### **NAMING CONVENTIONS**







### 16C UARTs with Intel databus interface

UART device	Channel	<b>&gt;</b>	Data rate at 5/3.3/2.5/1.8 V (Mbps)	Rx/Tx FIFO bytes	IrDA	Modem pins/channel	Rx/Tx FIFO INT trigger	RTS/CTS flow control	Software flow control	Power-down mode	Programmable Sampling rate	Package	Part number
SC16C550B	1	2.5-5.5 V	3.0/2.0/1.0/-	16	No	6	Four levels/None	Yes	No	No		PLCC44 LQFP48 DIP40	SC16C550BIA44 SC16C550BIB48 SC16C550BIN40
SC16C650B	1	2.5-5.5 V	3.0/2.0/1.0/-	32	Yes	6	Four levels/ Four levels	Yes	Yes	Yes		PLCC44 LQFP48 HVQFN32 DIP40	SC16C550BIBS SC16C650BIA44 SC16C650BIB48 SC16C650BIBS SC16C650BIN40
SC16C750B	1	2.5-5.5 V	3.0/2.0/1.0/-	16 or 64	No	6	Four levels/ None	Yes	No	Yes		PLCC44 LQFP64 HVQFN32	SC16C750BIA44 SC16C750BIB64 SC16C750BIBS
SC16C2550B	2	2.5-5.5 V	5.0/5.0/3.0/-	16	No	6	Four levels/None	No	No	No		PLCC44 LQFP48 DIP40 HVQFN32	SC16C2550BIA44 SC16C2550BIB48 SC16C2550BIN40 SC16C2550BIBS
SC16C2552B	2	2.5-5.5 V	5.0/5.0/3.0/-	16	No	6	Four levels/None	No	No	No		PLCC44	SC16C2552BIA44
604404500		0.5.5.1/	5 0 /5 0 /0 0 /	20	.,	,	5 1 1/5 1 1	.,	.,	.,		LQFP48	SC16C652BIB48
SC16C652B	2	2.5-5.5 V	5.0/5.0/3.0/-	32	Yes	6	Four levels/ Four levels	Yes	Yes	Yes		HVQFN32	SC16C652BIBS
SC16C752B	2	2.5-5.5 V	5.0/5.0/3.0/-	64	No	6	Programmable	Yes	Yes	Yes		LQFP48 HVQFN32	SC16C752BIB48 SC16C752BIBS
SC16C754B	4	2.5-5.5 V	5.0/5.0/3.0/-	64	No	6	Programmable	Yes	Yes	Yes		PLCC68 LQFP80 LQFP644	SC16C754BIA68 SC16C754BIB80 SC16C754IBIBM
SC16C554B	4	2.5-5.5 V	5.0/5.0/3.0/-	16	No	6	Four levels/None	Yes	No	No		LQFP64 LQFP64 LQFP80 HVQFN48 LQFP644	SC16C554BIA68 SC16C554BIB64 SC16C554BIB80 SC16C554BIBS SC16C554BIBM
SC16C654B	4	2.5-5.5 V	5.0/5.0/3.0/-	64	Yes	6	Four levels/ Four levels	Yes	Yes	Yes		LQFP64 LQFP644 LQFP644 LFBGA64 HVQFN48	SC16C654BIB64 SC16C654BIB64 SC16C654BIBM SC16C654BIEC SC16C654BIBS
SC16C850	1	2.5-3.3 V	-/5/5/-	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C850L	1	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C850SL	1	1.8 V	-/-/-20	128	Yes	6	Programmable	Yes	Yes	Yes	Yes	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C852	2	2.5-3.3 V	-/5/5/-	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36 LQFP48	SC16C852IBS SC16C852IET SC16C852IB
SC16C852L	2	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36 LQFP48	SC16C852IBS SC16C852IET SC16C852IB

### 16C UARTs with Motorola databus interface

UART device	Channel	> >	Data rate 5/3.3/2.5/1.8 V (Mbps)	Rx/Tx FIFO	IrDA	Modem pins / channel	Rx/Tx FIFO INT trigger	RTS/CTS flow control	Software Flow control	Power-down mode	Programmable Sampling rate	Package	Part number
SC16C850	1	2.5-3.3 V	-/5/5/-	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C850L	1	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C850SL	1	1.8 V	-/-/-20	128	Yes	6	Programmable	Yes	Yes	Yes	Yes	HVQFN32 TFBGA36	SC16C850IBS SC16C850IET
SC16C852	2	2.5-3.3 V	-/5/5/-	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36 LQFP48	SC16C852IBS SC16C852IET SC16C852IB
SC16C852L	2	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36 LQFP48	SC16C852IBS SC16C852IET SC16C852IB
SC68C850	1	2.5-3.3 V	-/5/5/-	128	Yes	6	Programmable	Yes	Yes	Yes	No	TFBGA36	SC16C850IET

### 16C UART with VLIO databus interface

UART device	Channel	>°	Data rate 5/3.3/2.5/1.8 V (Mbps)	Rx/Tx FIFO	IrDA	Modem pins / channel	Rx/Tx FIFO INT trigger	RTS/CTS flow control	Software Flow control	Power-down mode	Programmable Sampling rate	Package	Part number
SC16C850V	1	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32	SC16C850VIBS
SC16C850SV	1	1.8 V	-/-/-/20	128	Yes	6	Programmable	Yes	Yes	Yes	Yes	HVQFN32	SC16C850VIBS
SC16C852V	2	1.8 V	-/-/-/5	128	Yes	6	Programmable	Yes	Yes	Yes	No	HVQFN32 TFBGA36	SC16C852VIBS SC16C852VIET
SC16C852SV	2	1.8 V	-/-/-20	128	Yes	6	Programmable	Yes	Yes	Yes	Yes	TFBGA36	SC16C852SVIET

### 16C UART with I<sup>2</sup>C serial bus interface

UART device	Channel	>°	Data rate 5/3.3/2.5/1.8 V (Mbps)	Rx/Tx FIFO	IrDA	Modem pins / channel	GPIO pins	Rx/Tx FIFO INT trigger	RTS/CTS flow control	Software Flow control	Power-down mode	I <sup>2</sup> C Speed Max (Kbps)	Package	Part number
SC16IS740	1	2.5-3.3 V	-/5/3/-	64	Yes	2	0	Programmable	Yes	Yes	Yes	400	TSSOP16	SC16IS740IPW
SC16IS750	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	400	TSSOP24	SC16IS750IPW
SC16IS750	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	400	HVQFN24	SC16IS750IBS
SC16IS760	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	400	TSSOP24	SC16IS760IPW
SC16IS760	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	400	HVQFN24	SC16IS760IBS
SC16IS752	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4 [2]	Programmable	Yes	Yes	Yes	400	TSSOP28	SC16IS752IPW
SC16IS752	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4[2]	Programmable	Yes	Yes	Yes	400	HVQFN32	SC16IS752IBS
SC16IS762	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4[2]	Programmable	Yes	Yes	Yes	400	TSSOP28	SC16IS762IPW
SC16IS762	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4[2]	Programmable	Yes	Yes	Yes	400	HVQFN32	SC16IS762IBS
SC16IS850L	1	1.8 V	-/-/-/5	128	Yes	6	0	Programmable	Yes	Yes	Yes	400	HVQFN24	SC16IS850LIBS
SC16IS850L	1	1.8 V	-/-/-/5	128	Yes	6	0	Programmable	Yes	Yes	Yes	400	TSSOP24	SC16IS850LIPW

<sup>[1] 4</sup> pins are shared with modem pins

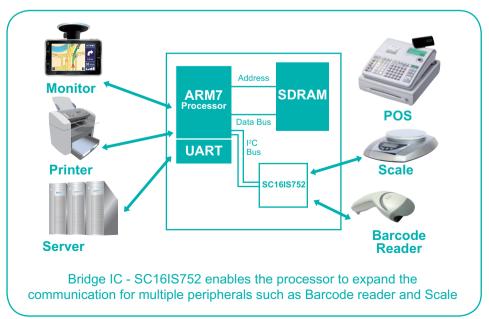
 $<sup>^{\</sup>left[ 2\right] }$  share with modem pins

### 16C UART with SPI serial bus interface

UART device	Channel	>8	Data rate 5/3.3/2.5/1.8 V (Mbps)	Rx/Tx FIFO	IrDA	Modem pins / channel	GPIO pins	Rx/Tx FIFO INT trigger	RTS/CTS flow control	Software Flow control	Power-down mode	SPI Speed Max (Mbps)	Package	Part number
SC16IS740	1	2.5-3.3 V	-/5/3/-	64	Yes	2	0	Programmable	Yes	Yes	Yes	4	TSSOP16	SC16IS740IPW
SC16IS750	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	4	TSSOP24	SC16IS750IPW
SC16IS750	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	4	HVQFN24	SC16IS750IBS
SC16IS760	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	15	TSSOP24	SC16IS760IPW
SC16IS760	1	2.5-3.3 V	-/5/3/-	64	Yes	6	8[1]	Programmable	Yes	Yes	Yes	15	HVQFN24	SC16IS760IBS
SC16IS752	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4 [2]	Programmable	Yes	Yes	Yes	4	TSSOP28	SC16IS752IPW
SC16IS752	2	2.5-3.3 V	-/5/3/-	64	Yes	6	<b>4</b> <sup>[2]</sup>	Programmable	Yes	Yes	Yes	4	HVQFN32	SC16IS752IBS
SC16IS762	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4 <sup>[2]</sup>	Programmable	Yes	Yes	Yes	15	TSSOP28	SC16IS762IPW
SC16IS762	2	2.5-3.3 V	-/5/3/-	64	Yes	6	4 [2]	Programmable	Yes	Yes	Yes	15	HVQFN32	SC16IS762IBS
SC16IS850L	1	1.8 V	-/-/-/5	128	Yes	6	0	Programmable	Yes	Yes	Yes	15	TSSOP24	SC16IS850LIPW
SC16IS850L	1	1.8 V	-/-/-/5	128	Yes	6	0	Programmable	Yes	Yes	Yes	15	HVQFN24	SC16IS850LIBS

<sup>[1] 4</sup> pins are shared with modem pins

 $<sup>^{\</sup>mbox{\tiny [2]}}$  share with modem pins



### Exar competitive cross-reference

	•					
EXAR Part Number	Exar Description	Exar Package	Cross Type	NXP Part Number	NXP Description	
SINGLE UART						
ST16C550CJ44-F or IJ44-F	Single Channel UART with 16-Byte FIFO	PLCC-44	Drop-in	SC16C550BIA44	2.5 V-5 V single UART with 16-Byte FIFO	
ST16C550CP40 or IP40	Single Channel UART with 16-Byte FIFO	PDIP-40	Drop-in	SC16C550BIN40	2.5 V-5 V single UART with 16-Byte FIFO	
ST16C550CQ48-F or IQ48-F	Single Channel UART with 16-Byte FIFO	TQFP-48	Drop-in	SC16C550BIB48	2.5 V-5 V single UART with 16-Byte FIFO	
XR16L570IL32-F	Smallest 1.62 V to 5.5 V UART with 16-Byte FIFO and PowerSave	QFN-32	Similar Part	SC16C650BIBS	2.5 V-5 V single UART with 32-Byte FIFO	
ST16C580CQ48-F or IQ48-F	UART with 16-Byte FIFO and IrDA Encoder/Decoder	TQFP-48	Similar Part	SC16C550BIB48	2.5 V-5 V single UART with 16-Byte FIFO	
XR16L580IL-F	Smallest 2.25 V to 5.5 V UART with 16-Byte FIFO and PowerSave	QFN-32	Similar Part	SC16C550BIBS	2.5 V-5 V single UART with 16-Byte FIFO	
XR16M581IL24-F	1.62 V to 3.63 V UART with 16-byte FIFO, VLIO interface	QFN-24	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M581IL32-F	1.62 V to 3.63 V UART with 16-byte FIFO, VLIO interface	QFN-32	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M581IB25-F	1.62 V to 3.63 V UART with 16-byte FIFO, VLIO interface	BGA-25	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M681IL24-F	1.62 V to 3.63 V UART with 32-byte FIFO, VLIO interface	QFN-24	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M681IL32-F	1.62 V to 3.63 V UART with 32-byte FIFO, VLIO interface	QFN-32	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M681IB25	1.62 V to 3.63 V UART with 32-byte FIFO, VLIO interface	BGA-25	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M781IL24-F	1.62 V to 3.63 V UART with 64-byte FIFO, VLIO interface	QFN-24	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M781IL32-F	1.62 V to 3.63 V UART with 64-byte FIFO, VLIO interface	QFN-32	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
XR16M781IB25-F	1.62 V to 3.63 V UART with 64-byte FIFO, VLIO interface	BGA-25	Similar Part	SC16C850VIBS	1.8 V UART with 128-byte FIFO, VLIO Interface	
ST16C650ACJ44-F or IJ44-F	2.90 V to 5.5 V UART with 32-Byte FIFO	PLCC-44	Drop-in	SC16C650BIA44	2.5 V-5 V single UART with 32-Byte FIFO	
ST16C650ACQ48-F or IQ48-F	2.90 V to 5.5 V UART with 32-Byte FIFO	TQFP-48	Drop-in	SC16C650BIB48	2.5 V-5 V single UART with 32-Byte FIFO	
XR16C850CM-F or IM-F	2.97 V to 5.5 V UART with 128-Byte FIFO	TQFP-48	Similar Part	SC16C850IBS	2.5 V-3.3 V single UART with 128-Byte FIFO	
XR16C850CM-F or IM-F	2.97 V to 5.5 V UART with 128-Byte FIFO	TQFP-48	Similar Part	SC16C850IET	2.5 V-3.3 V single UART with 128-Byte FIFO	
XR16C850CJ-F or IJ-F	2.97 V to 5.5 V UART with 128-Byte FIFO	PLCC-44	Similar Part	SC16C850IBS	2.5 V-3.3 V single OAKT With 128-Byte FIFO	
XR16C850CJ-F or IJ-F		PLCC-44	Similar Part	SC16C850IET	-	
	2.97 V to 5.5 V UART with 128-Byte FIFO	PLCC-44	Similar Part	3C16C6301E1	2.5 V-3.3 V single UART with 128-Byte FIFO	
DUAL UART	207 VA- F F V DUART with 1/ Pota FIFO	DDID 40	Dana ia	CC1/C2FF0DINIAO	2 F.V. F.V. dural HADT with 1/ Duran FIFO	
ST16C2550CP40-F or IP40-F	2.97 V to 5.5 V DUART with 16-Byte FIFO	PDIP-40	Drop-in	SC16C2550BIN40	2.5 V-5 V dual UART with 16-Byte FIFO	
ST16C2550CQ48-F or IQ48-F	2.97 V to 5.5 V DUART with 16-Byte FIFO	TQFP-48	Drop-in	SC16C2550BIB48	2.5 V-5 V dual UART with 16-Byte FIFO	
ST16C2550CJ44-F or IJ44-F	2.97 V to 5.5 V DUART with 16-Byte FIFO	PLCC-44	Drop-in	SC16C2550BIA44	2.5 V-5 V dual UART with 16-Byte FIFO	
ST16C2552CJ44-F or IJ44-F	2.97 V to 5.5 V DUART with 16-Byte FIFO	PLCC-44	Drop-in	SC16C2552BIA44	2.5 V-5 V dual UART with 16-Byte FIFO	
XR16L2551IM-F	2.25 V to 5.5 V DUART with 16-Byte FIFO	TQFP-48	Similar Part	SC16C652BIB48	2.5 V-5 V dual UART with 32-Byte FIFO	
XR16L2552IJ-F	2.25 V to 5.5 V DUART with 16-Byte FIFO	PLCC-44	Similar Part	SC16C2552BIA44	2.5 V-5 V dual UART with 16-Byte FIFO	
XR16V2650IM-F	High Performance DUART with 32-Byte FIFO	TQFP-48	Similar Part	SC16C652BIB48	2.5 V-5 V dual UART with 32-Byte FIFO	
XR16V2650IL-F	High Performance DUART with 32-Byte FIFO	QFN-32	Similar Part	SC16C652BIBS	2.5 V-5 V dual UART with 32-Byte FIFO	
XR16V2651IM-F	High Performance DUART with 32-Byte FIFO	TQFP-48	Similar Part	SC68C652BIB48	2.5 V-5 V dual UART with 32-Byte FIFO	
XR16M2750IM48-F	High Performance Low Voltage DUART with 64-Byte FIFO	TQFP-48	Similar Part	SC16C752BIB48	2.5 V-5 V dual UART with 64-Byte FIFO	
XR16M2750IL32-F	High Performance Low Voltage DUART with 64-Byte FIFO	QFN-32	Similar Part	SC16C752BIBS	2.5 V-5 V dual UART with 64-Byte FIFO	
XR16M752IM48-F	High Performance DUART with 64-Byte FIFO	TQFP-48	Similar Part	SC16C752BIB48	2.5 V-5 V dual UART with 64-Byte FIFO	
XR16M752IL32-F	High Performance DUART with 64-Byte FIFO	QFN-32	Similar Part	SC16C752BIBS	2.5 V-5 V dual UART with 64-Byte FIFO	
XR68M752IM48-F	1.62 V to 3.6 V high Performance Dual UART with 64-Byte FIFO	TQFP-48	Drop-in	SC68C752BIB48	1.62 V to 3.6 V high Performance Dual UART with 64-Byte FIFO	
QUAD UART						
XR16V554IV-F	High Performance Quad UART with 16-Byte FIFO	LQFP-64	Drop-in	SC16C554BIB64	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V554IV80-F	High Performance Quad UART with 16-Byte FIFO	LQFP-80	Similar Part	SC16C554BIB80	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V554IV-F	High Performance Quad UART with 16-Byte FIFO	LQFP-64	Drop-in	SC16C554BIBM	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V564IV-F	High Performance Quad UART with 32-Byte FIFO	LQFP-64	Similar Part	SC16C554BIBM	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V554IL-F	High Performance Quad UART with 16-Byte FIFO	QFN-48	Similar Part	SC16C554BIBS	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V554IJ-F	High Performance Quad UART with 16-Byte FIFO	PLCC-68	Drop-in	SC16C554DBIA68	2.5 V-5 V quad UART with 16-Byte FIFO	
XR16V554DIV-F	High Performance Quad UART with 16-Byte FIFO	LQFP-64	Drop-in	SC16C554DBIB64	2.5 V-5 V quad UART with 16-Byte FIFO	
ST16C654CJ68-F or IJ68-F	2.25 V to 5.5 V Quad UART with 64-Byte FIFO	PLCC-68	Drop-in	SC16C654BIA68	2.5 V-5 V quad UART with 64-Byte FIFO	
ST16C654CQ64-F or IQ64-F	2.25 V to 5.5 V Quad UART with 64-Byte FIFO	LQFP-64	Drop-in	SC16C654BIB64	2.5 V-5 V quad UART with 64-Byte FIFO	
ST16C654CJ68-F or IJ68-F	2.25 V to 5.5 V Quad UART with 64-Byte FIFO	PLCC-68	Similar Part	SC16C754BIA68	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16V654IV-F	High Performance Quad UART with 64-Byte FIFO	LQFP-64	Similar Part	SC16C654BIB64	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16V654IL-F	High Performance Quad UART with 64-Byte FIFO	QFN-48	Similar Part	SC16C654BIBS	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16V654IV-F	High Performance Quad UART with 64-Byte FIFO	LQFP-64	Similar Part	SC16C654BIEC	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16V654IJ-F	High Performance Quad UART with 64-Byte FIFO	PLCC-68	Similar Part	SC16C754BIA68	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16V654IV80-F	High Performance Quad UART with 64-Byte FIFO	LQFP-80	Similar Part	SC16C754BIB80	2.5 V-5 V quad UART with 64-Byte FIFO	

	NXP Package	COMMENTS IF NOT DROP-IN REPLACEMENT	HARDWARE CHANGE	SOFTWAR CHANGE
	PLCC44		NO	NO
	DIP40		NO	NO
	LQFP48		NO	NO
	HVQFN32	Pin map not compatible. Software similar to the SC16C650B. XR16L570 has 16-byte FIFO while SC16C650 has 32-byte FIFO.	YES	YES
	LQFP48	Pin map compatible. Similar UART functions but the NXP part does not support hardware, software flow control and IrDA	NO	YES
	HVQFN32	pin map not compatibe, similar software, SC16C550 does not have DREV and DVID registers	YES	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	HVQFN32	Pin map compatible, basic UART functions compatible, both has VLIO host interface	NO	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	HVQFN32	Pin map compatible, basic UART functions compatible, both has VLIO host interface	NO	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	HVQFN32	Pin map compatible, basic UART functions compatible, both has VLIO host interface	NO	YES
	HVQFN32	Pin map NOT compatible, basic UART functions compatible, both has VLIO host interface	YES	YES
	PLCC44		NO	NO
	LQFP48		NO	NO
	HVQFN32	pin map not compatible, basic UART function compatible, both has 128-byte FIFOs	YES	YES
	TFBGA36	pin map not compatible, basic UART function compatible, both has 128-byte FIFOs	YES	YES
	HVQFN32	pin map not compatible, basic UART function compatible, both has 128-byte FIFOs	YES	YES
	TFBGA36	pin map not compatible, basic UART function compatible, both has 128-byte FIFOs	YES	YES
	DIP40		NO	NO
	LQFP48		NO	NO
	PLCC44		NO	NO
	PLCC44		NO	NO
	LQFP48	pin map similar: SC16C652B does not have PwrSave pin, 16/-68 pin. Software similar: XR16L2551 does not support FCR[5:4], or Xon any and XR16L2551 has 16-byte FIFO	YES	YES
	PLCC44	Pin  map  not  compatible.  Similar  UART  functions,  but  the  NXP  part  does  not  support  hardware  and  software  flow  control  and  software  flow  control  and  software  flow  control  and  software  flow  control  fl	YES	YES
	LQFP48	Pin map compatible and SC16C652 does not have DLD, DREV and DVID registers	NO	YES
1	HVQFN32	Pin map not compatible and SC16C652 does not have DLD, DREV and DVID registers	YES	YES
	LQFP48	pin map similar: SC16C652B does not have PwrSave pin, 16/-68 pin. Software similar: XR16L2551 does not support FCR[5:4], or Xon any.	YES	YES
	LQFP48	pin map compatible. SC16C752B does not have these registers: EMSR, FLVL, DREV, DVID, TRG, FC, FCTR	NO	YES
	HVQFN32	pin map NOT compatible. SC16C752B does not have these registers: EMSR, FLVL, DREV, DVID, TRG, FC, FCTR	YES	YES
	LQFP48	pin map compatible. Software similar but XR16M752IM48 has Fractional baud rate generator and 4X, 8X sampling rates	NO	YES
	HVQFN32	pin map not compatible. Software similar but XR16M752IL32 has Fractional baud rate generator and 4X, 8X sampling rates	YES	YES
	LQFP48		NO	NO
	LQFP64		NO	NO
	LQFP80	Pin map not compatible. Software compatible to the SC16C554BIB80	YES	NO
	LQFP64		NO	NO
	LQFP64	Pin to pin compatible. XR16V564IV has larger 32 byte FIFOs.	NO	YES
	HVQFN48	Pin map not compatible. XR16V554IL has TX, RX, RTS, CTS for all 4 channels.	YES	YES
	PLCC68		NO	NO
	LQFP64		NO	NO
	PLCC68		NO	NO
	LQFP64		NO	NO
	PLCC68	pin map compatible. Automatic hardware flow control enabled differently.	NO	YES
	LQFP64	pin map compatible. Software compatible but SC16C654BIB64 does not support 4X, 8X sampling rates and FSTAT register	NO	YES
	HVQFN48	pin map not compatible (XR16V654IL has TX, RX, RTS, CTS for all 4 channels). Software compatible but the NXP part does not support 4X, 8X sampling rates and FSTAT register	YES	YES
	LEDC A / A	pin map not compatible. Software compatible but the NXP part does not support 4X, 8X sampling rates and FSTAT register	YES	YES
	LFBGA64			
	PLCC68	pin map compatible. Automatic hardware flow control enabled differently.SC16C754B does not have DLD (4X, 8X sampling rates), FSTAT (FIFO status) registers	NO	YES

### Exar competitive cross-reference

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EXAR Part Number	Exar Description	Exar Package	Cross Type	NXP Part Number	NXP Description	
XR16V654IV-F	High Performance Quad UART with 64-Byte FIFO	LQFP-64	Similar Part	SC16C754BIBM	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16L784CV-F or IV-F	High Performance 2.97 V to 5.5 V Quad UART with 64-Byte FIFO	LQFP-64	Similar Part	SC16C754BIB64	2.5 V-5 V quad UART with 64-Byte FIFO	
XR16L784CV-F or IV-F	High Performance 2.97 V to 5.5 V Quad UART with 64-Byte FIFO	LQFP-64	Similar Part	SC28L194A1BE	Quart channel UART with 16-byte FIFO	
XR16L788CQ-F or IQ-F	High Performance 2.97 V to 5.5 V Octal UART with 64-Byte FIFO	QFP-100	Func Equiv	SC28L198A1BE	Octal channel UART with 16-byte FIFO	
XR88C92CJ-F	Dual Channel UART with 8-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 8-Byte FIFO	
XR88C92IJ-F	Dual Channel UART with 8-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 8-Byte FIFO	
XR88C92CV-F	Dual Channel UART with 8-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 8-Byte FIFO	
XR88C92IV-F	Dual Channel UART with 8-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 8-Byte FIFO	
XR88C192CJ-F	Dual Channel UART with 16-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 16-Byte FIFO	
XR88C192IJ-F	Dual Channel UART with 16-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 16-Byte FIFO	
XR88C192CV-F	Dual Channel UART with 16-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 16-Byte FIFO	
XR88C192IV-F	Dual Channel UART with 16-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 16-Byte FIFO	
XR88C681CP/28-F	Dual Channel UART	PDIP-28	Similar Part	SCC2692AC1N28	Dual Channel UART	
XR88C681P/28-F	Dual Channel UART	PDIP-28	Similar Part	SCC2692AE1N28	Dual Channel UART	
XR88C681CP/40-F	Dual Channel UART	PDIP-40	Similar Part	SCC2692AC1N40	Dual Channel UART	
XR88C681P/40-F	Dual Channel UART	PDIP-40	Similar Part	SCC2692AE1N40	Dual Channel UART	
XR88C681CJ-F	Dual Channel UART	PLCC-44	Similar Part	SCC2692AC1A44	Dual Channel UART	
XR88C681J-F	Dual Channel UART	PLCC-44	Similar Part	SCC2692AE1A44	Dual Channel UART	
XR68C92CJ-F	Dual Channel UART with 8-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 8-Byte FIFO	
XR68C92IJ-F	Dual Channel UART with 8-Byte FIFO	PLCC-44	Similar Part	SC28L92A1A	Dual Channel UART with 8-Byte FIFO	
XR68C92CV-F	Dual Channel UART with 8-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 8-Byte FIFO	
XR68C92IV-F	Dual Channel UART with 8-Byte FIFO	LQFP-44	Similar Part	SC28L92A1B	Dual Channel UART with 8-Byte FIFO	
XR68C681CP-F	Dual Channel UART	PDIP-40	Similar Part	SCC68692C1N40	Dual Channel UART	
XR68C681P-F	Dual Channel UART	PDIP-40	Similar Part	SCC68692E1N40	Dual Channel UART	
XR68C681CJ-F	Dual Channel UART	PLCC-44	Similar Part	SCC68692C1A44	Dual Channel UART	
XR68C681J-F	Dual Channel UART	PLCC-44	Similar Part	SCC68692E1A44	Dual Channel UART	
UART WITH I <sup>2</sup> C/SPI IN	TERFACE					
XR20M1170IL28-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	28-QFN	Similar Part	SC16IS760IBS	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1170IL24-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	24-QFN	Similar Part	SC16IS760IBS	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1170IL16-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	16-QFN	Similar Part	SC16IS740IPW	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1170IG24-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	24-TSSOP	Similar Part	SC16IS760IPW	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1170IG16-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	16-TSSOP	Similar Part	SC16IS740IPW	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20V2170IL40-F	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface with RS232 drivers	40-QFN	Similar Part	SC16IS760IBS	Single Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1172IL32-F	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	32-QFN	Similar Part	SC16IS762IBS	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20M1172IG28-F	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	28-TSSOP	Similar Part	SC16IS762IPW	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	
XR20V2172IL64-F	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface with RS232 drivers	64-QFN	Similar Part	SC16IS762IBS	Dual Channel UART, 64-byte FIFO, I <sup>2</sup> C/SPI interface	

DFR64 pin map compatible. Automate hardware flow control enabled differently. SC16C/5818 does not have DLD (4X, 8X sampling rated).  Different pin out. XR16L784 has global interrupt registers and SC16C/584 does not.  Different package and pinout. XR16L784 has 16C550 compatible register while SC28L194 does not. Both have global interrupt registers.  VES  VES  UQFP00 Different package and pinout. XR16L788 has 16C550 compatible register while SC28L198 does not. Both have global interrupt registers.  VES  VES  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Tag section).  PLCC44 Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used as a master in RS-485 mode (see Ear distablent Exts Storage For The A/D Ta				
LOFFISH  Different processors princes. 28TH ACR8 has pideal interrupt register and SC MACR8 does not.  DIfferent package and princes. 28TH ACR8 has 16CSSO compatible register while SC28TH 48 coses not. Both has good interrupt registers  VES  VES  LOFFISO  Different package and princes. 28TH ACR8 has 16CSSO compatible register while SC28TH 48 coses not. Both has good interrupt registers  VES  VES  LOFFISO  Different package and princes. 28TH ACR8 has 16CSSO compatible register while SC28TH 48 coses not. Both has good interrupt registers  VES  VES  HIGGS4  Most likely drap inepticement. File may compatible (30M pix can be left inconnected). A minor difference when SC28TH 28 userd  as materia in EAS form does be for individual contents. The major compatible (30M pix can be left inconnected). A minor difference when SC28TH 28 userd  as materia in EAS form does be for individual contents. The ACR Ingredient of the A		COMMENTS IF NOT DROP-IN REPLACEMENT		SOFTWARE CHANGE
Different peckage and prince. XREA/DR has 16:550 compatible register while 5:0281/PR does not. Both has global interrupt registers  VES  VES  (OPPED  Different parkage and prince. XREA/DR has MCSS0 compatible register while 5:0281/PR does not. British have girchal interrupt registers  VES  VES  ROCK4  Most likely door in registerment. Prince compabile VM price and be full accordance of the Prince of Prince Area Scale VS as used as a matter in ScAP who does be for districted Last a Stonage of The Prince Prince when \$0280/20 is used as a matter in ScAP who does be for districted Last a Stonage of The Prince Prince when \$0280/20 is used as a matter in ScAP who does be for districted Last a Stonage of The Prince Prince when \$0280/20 is used as a matter in ScAP who does be for districted Last a Stonage of The Prince Prince when \$0280/20 is used as a matter in ScAP who does be for districted Last a Stonage of The Prince Prince when \$0280/20 is used as a matter in ScAP who does be for districted Last a Stonage of the Prince Prince when \$0280/20 is used as matter in ScAP who does be for districted Last a Stonage of the Prince Prince when \$0280/20 is used as matter in ScAP who does be for districted Last a Stonage of the Prince Prince when \$0280/20 is used as matter in ScAP who does be an districted Last a Stonage of the Prince when \$0280/20 is used as matter in ScAP with the Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Prince Prince Prince when \$0280/20 is used as matter in ScAP with the Prince Princ	LQFP64		NO	YES
Different pedage and pirous. XR14289 has 162530 compatible register while 5C28198 does not. Both have global interrupt registers.  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  No. MIGHT NOT  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  No. MIGHT NOT  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  No. MIGHT NOT  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  No. No.  No. No.  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  A market in RSS drov disc loss accessed. A minor and fiftee and when 5C28292 a used.  No. No.  No. No.  PLCC44  Most likely drop in replacement. Prin hap compatible IVM prin can be left unconnected. A minor difference when 5C28292 a used.  A market in RSS drov disc loss accessed. A minor difference when 5C28292 is used as a market in RS-405 more left to make the second of the IVM by genotic in the IVM print I	LQFP64	Different pin out. XR16L784 has global interrupt register and SC16C754 does not	YES	YES
PLCC44 Most likely drop in replacement. Prin map compatible UM prin can be left unconnection(). A minor difference when SC28-92 is used on more in 185-455 mode) price between disconnected Carlo Strongly for This A PD Tigg section?  PLCC44 Most likely drop in replacement. Prin map compatible UM prin can be left unconnection(). A minor difference when SC28-92 is used on sometime of Sc28-92 is used on sometime of Sc28-92 is used on the scale of the APD Tigg section ?  PLCC44 Most likely drop in replacement. Prin map compatible UM price as bell with accommended. A minor difference when SC28-92 is used in No.	LQFP80	Different package and pinout. XR16L784 has 16C550 compatible register while SC28L194 does not. Both has global interrupt registers	YES	YES
PLCC44 Most likely drop in replacement. Prin map compatible UM prin can be left unconnection(). A minor difference when SC28-92 is used on more in 185-455 mode) price between disconnected Carlo Strongly for This A PD Tigg section?  PLCC44 Most likely drop in replacement. Prin map compatible UM prin can be left unconnection(). A minor difference when SC28-92 is used on sometime of Sc28-92 is used on sometime of Sc28-92 is used on the scale of the APD Tigg section ?  PLCC44 Most likely drop in replacement. Prin map compatible UM price as bell with accommended. A minor difference when SC28-92 is used in No.				
PICC44  Most tilely drop in replacement Pinn posi compatible Mily in cent beilt immorated. A min of disease aware SCB-97 is used as an anster in IS-485 mode lose Exa datasheet Exas Sorage For The AD Tag section)  Most tilely drop in replacement Pinn posi compatible Mily in cent beilt immorated. A min of disease aware SCB-97 is used as an anster in IS-485 mode lose Exa datasheet Exas Sorage For The AD Tag section)  Most tilely drop in replacement Pinn posi compatible Mily in cent beilt immorated. A min of disease aware SCB-97 is used as an anster in IS-485 mode lose Exa datasheet Exas Sorage For The AD Tag section)  Most tilely drop in replacement Pinn posi compatible Mily in cent beilt immorated. A min of disease aware SCB-97 is used as an anster in IS-485 mode lose Exas datasheet Exas Sorage For The AD Tag section)  Most tilely drop in replacement Pinn posi compatible Mily in cent beilt immorated. A min of disease are arrest in IS-485 mode lose Exas datasheet Exas Sorage For The AD Tag section). It is byte PinO must be enabled via Milkin agreement of the Standard Milking Milki	LQFP100	Different package and pinout. XR16L788 has 16C550 compatible register while SC28L198 does not. Both have global interrupt registers	YES	YES
PLCC44 Most likely drop in registerance. The map compatible (Multiple can be left unconnected). A mile difference when SC28192 is used as a material mile-85 cmolecule for advantable climater page for the APID page section?  APPL Most likely drop in registerance. The map compatible (Multiple can be left unconnected). A mile difference when SC28192 is used as material mile-85 cmolecule for advantable climater page for the APID page section?  APPL Most likely drop in registerance. The map compatible (Multiple can be left unconnected). A mile difference when SC28192 is used as a material mile-45 mode for the APID page section. It is page from the APID page section. It is page	PLCC44		NO	MIGHT NOT
GPH44  Most Blady drop in reglacement in St485 mode (see Exar disablent Extra Storage For The A/D Tay section)  PLCC44  Most Blady drop in reglacement. Private prographical (M) in care be left numeroscited. A mice of difference when SCRIR 92's used as a master in SC-485 mode (see Exar disablent Extra Storage For The A/D Tay section)  PLCC44  Most Blady drop in reglacement. Private prographical (A) in care be left numeroscited. A mice of difference when SCRIR 92's used as a master in SC-485 mode (see Exar disablent Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement. Private prographical (A) in care be left numeroscited. A mice of MRR register mode (see Exar disablent Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement. Private prographical (A) in part is be left numeroscited. A mice of MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in reglacement Extra Storage For The A/D Tay section) 16-byte FEO must be enabled via MRR register.  Most Blady drop in register and different prograph 18-byte FEO must be commanded register and efferent.  NO YES  DPP32 SCC2892 does not support Maked interrupt Status and interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  FECC44 UMp not	PLCC44	Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used	NO	MIGHT NOT
PLCC44  Most likely drog in replacement. This may compasible IMD may cannot be left unconnected. A micro difference when SC22812 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). A micro difference when SC22812 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). 1-b-yps F1FOr most be enabled via M80 register.  PLCC44  Most likely drog in replacement. Prim and the Internocented A micro difference when SC22812 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). 1-b-yps F1FOr most be enabled via M80 register.  Most likely drog in replacement. Prim a compasible IMD may can be left unconnected. A micro difference when SC22802 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). 1-b-yps F1FOr most be enabled via M80 register.  Most likely drog in replacement. Prim a compasible IMD may can be left unconnected. A micro difference when SC22802 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). 1-b-yps F1FOr most be enabled via M80 register.  Most likely drog in replacement. Prim a most being unconnected. A micro difference when SC22802 as used as a master in RS-485 mode (see Exar databaset Exar Storage For The A/D Tag section). 1-b-yps F1FOr most be enabled via M80 registers are different.  Most likely drog in replacement. Prim a most being unconnected. A micro difference when SC2802 via via M80 registers are different.  NO YES  SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The last B commands of the command registers are different.  NO YES  PLCC44  SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The last B commands of the command registers are different.  NO YES  SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The last B commands of the command registers are different.  NO YES  PLCC44  I/W pin of 28892 must be c	QFP44	Most likely drop in replacement. Pin map compatible (I/M pin can be left unconnected). A minor difference when SC28L92 is used	NO	NO
PLCC44 Most likely treip in eligenement. Prin my compatible (All) part can be left unconsciouted. A minimal efficience when SC22862 years and as a mater in RS-485 mode (see Exar distance Exis Storage For The A/D Tag section). 16-byte FFO must be enabled via M80 register mode (see Exar distance Exis Storage For The A/D Tag section). 16-byte FFO must be enabled via M80 register mode (see Exar distance Exis Storage For The A/D Tag section). 16-byte FFO must be enabled via M80 register mode (see Exar distance Exis Storage For The A/D Tag section). 16-byte FFO must be enabled via M80 register.  Most likely drop in replacement. Prin map compatible (M) prin can be left unconnected). A minimal difference when exal as a mater in RS-485 mode (see Exar distance Exis Storage For The A/D Tag section). 16-byte FFO must be enabled via M80 register.  DIP28 SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES MIGHT NOT	QFP44		NO	NO
Most likely drop in replacement Pin map compatible (M) in part of the property	PLCC44		NO	YES
Most likely drop in placement. Pin map compatible (My Pin can be left unconnected). A mirror difference with SC28/12 used as a master in RS-485 mode (see Exar datashee Exar Storage For The ADT Tag section). 1-6-type FIPO must be enabled via MR0 register.  DIP28 SCC28/92 does not support Masked interrupt Status and interrupt Vector registers. The last 8 commands of the command registers are different. NO YES DIP80 SCC28/92 does not support Masked interrupt Status and interrupt Vector registers. The last 8 commands of the command registers are different. NO YES DIP80 SCC28/92 does not support Masked interrupt Status and interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC28/92 does not support Masked interrupt Status and interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC28/92 does not support Masked interrupt Status and Interrupt Vector registers. The Status Status and	PLCC44		NO	YES
mode (see Exar datasheet Extra Storage For The A/D Tag section). 10-byte FIFO must be enabled via MRD register NO YES  DIP28 SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The least 8 commands of the command registers are different. NO YES  DIP40 SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The least 8 commands of the command registers are different. NO YES  DIP40 SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The least 8 commands of the command registers are different. NO YES  PLCC44 SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The least 8 commands of the command registers are different. NO YES  PLCC44 SCC2892 does not support Masked Interrupt Status and Interrupt Vector registers. The least 8 commands of the command registers are different. NO YES  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28202 is used an amaster in 85-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28102 is used as a master in R5-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28102 is used as a master in R5-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28102 is used as a master in R5-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28102 is used as a master in R5-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC28102 is used as a master in R5-485 mode  (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A min	QFP44		NO	YES
DIP28 SCC2692 does not support Masked interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES DP40 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC442 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC2802 is used as a master in R5-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP45 SCC68692 doe	QFP44		NO	YES
DIP40 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasableet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasable Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasable Extra	DIP28	SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.	NO	YES
DIP40 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  QFP44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  QFP44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  QFP44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  QFP44 I/M pin of 28192 must be connected to ground. A minor difference when SC28192 is used as a master in RS-485 mode (see Exar datashee Extra Storage For The A/D Tag section)  QFP44 I/M pin of 28192 must be connected to ground. A minor difference when Drag section of the Command registers are different. NO YES DIP40 SCC68692 does not support Masked Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. T	DIP28	SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.	NO	YES
PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  PLCC44 SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  PLCC44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  OFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  OFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  OFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  OFP44 I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar databatheet Extra Storage For The A/D Tag section)  DIP40 SCC68692 does not support Masked Interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  HVOFN24 Different pin map. SC166760 does not have these features: ENR and EN485 pins, 8X sampling rate, fractional band rate generator. SC166760 supports  YES  YES  TSSOP16 Different pin map. SC166760 does not have these features: 8X sampling rate, fractional band rate generator. Sc166760 supports SPI speed up to 15 MH while XR20M1170 supports	DIP40	SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.	NO	YES
PLCC44  SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO  YES  MIGHT NOT  PLCC44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  PLCC44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to generate the section of the S	DIP40	$SCC 2692\ does\ not\ support\ Masked\ Interrupt\ Status\ and\ Interrupt\ Vector\ registers.\ The\ last\ 8\ commands\ of\ the\ command\ registers\ are\ different.$	NO	YES
PLCC44  I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  PLCC44  I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  DIP40  SCC68692 does not support Masked interrupt Status and Extra Storage For The A/D Tag section)  PLCC44  SCC68692 does not support Masked interrupt Status and Interrupt Vectro registers. The last 8 commands of the command registers are different.  NO  YES  PLCC44  SCC68692 does not support Masked interrupt Status and Interrupt Vectro registers. The last 8 commands of the command registers are different.  NO  YES  PLCC44  SCC68692 does not support Masked interrupt Status and Interrupt Vectro registers. The last 8 commands of the command registers are different.  NO  YES  HVGFN24  Different pin map. SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M170 supports SPI speed of 5 MH max. Software compatible  YES  YES  YES  Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M170 supports SPI speed of 5 MH max. Software compatible  YES  YES  TSSOP16  Different pin map. SC16IS760 does not have these feat	PLCC44	${\sf SCC2692\ does\ not\ support\ Masked\ Interrupt\ Status\ and\ Interrupt\ Vector\ registers.\ The\ last\ 8\ commands\ of\ the\ command\ registers\ are\ different.}$	NO	YES
FILCULAR   1/M pin of 281.92 must be connected to ground. A minor difference when SC281.92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)   YES   MIGHT NOT	PLCC44	SCC2692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.	NO	YES
Gese Exar datasheet Extra Storage For The A/D Tag section)  GPP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  OFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  DIP40  SCC68692 does not support Masked Interrupt Status and Status Stat	PLCC44		YES	MIGHT NOT
Gee Exar datasheet Extra Storage For The A/D Tag section)  QFP44  I/M pin of 28L92 must be connected to ground. A minor difference when SC28L92 is used as a master in RS-485 mode (see Exar datasheet Extra Storage For The A/D Tag section)  DIP40  SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO  YES  DIP40  SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO  YES  PLCC44  SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO  YES  PLCC44  SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO  YES  HVQFN24  Different pin map. SC16I5760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16  Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH will KR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP24  Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH will KR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP26  Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH wills KR20M1172 supports SPI speed of 5 MH max. Software compatible  Use SC16IS760IBS + external RS-232 drivers  VES  VES  HVQFN24  Different pin map. SC16IS740 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH will	PLCC44		YES	MIGHT NOT
(see Exar datasheet Extra Storage For The A/D Tag section)  DIP40 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  YES  YES  YES  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. Sc16IS760 supports SPI speed up to 15 MH will XR20M1170 supports SPI speed of 5 MH max. Software compatible  YES YES  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH wille XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES YES  HVQFN32 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fra	QFP44		YES	MIGHT NOT
DIP40 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES SCC68692 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible YES YES SCC686760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible YES YES SPI SCC686760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports YES YES SPI SPI SPI SPI SPI SPI SPI SPI SPI SP	QFP44		YES	MIGHT NOT
PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.  NO YES  HVQFN24 Different pin map. SC16/S760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16/S760 supports  SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  HVQFN24 Different pin map. SC16/S760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16/S760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16/S760 does not have these features: 8X sampling rate, fractional baud rate generator. Sc16/S760 supports SPI speed up to 15 MH yes  While XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16/S760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16/S760 supports SPI speed up to 15 MH yes  YES  YES  HVQFN24 Use SC16/S760 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES  YES  HVQFN32 Different pin map. SC16/S762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16/S762 supports  YES  YES  YES  Different pin map. SC16/S762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16/S762 supports  YES  YES  PLOGEN SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES  YES  YES  PLOGEN SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible				
PLCC44 SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different. NO YES  HVQFN24 Different pin map. SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  HVQFN24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH yes YES  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible  TSSOP24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH yes While XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES YES  HVQFN24 Use SC16IS760IBS + external RS-232 drivers  YES YES  HVQFN32 Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES YES  YES  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports YES YES  YES  YES  YES  YES  YES  YES				
HVQFN24 Different pin map. SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  HVQFN24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. Sc16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  TSSOP16 Use SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  TSSOP18 Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES YES  YES YES				
HVQFN24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible  TSSOP24 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES YES  HVQFN24 Use SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible	PLCC44	SCC68692 does not support Masked Interrupt Status and Interrupt Vector registers. The last 8 commands of the command registers are different.	NO	YES
HVQFN24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible  TSSOP24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES YES  HVQFN24 Use SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  YES YES  HVQFN32 Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports  YES YES  YES YES  YES YES	HVQFN24		YES	YES
TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible  TSSOP24 Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH while XR20M1170 supports SPI speed of 5 MH max. Software compatible  TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES YES  HVQFN24 Use SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  YES YES  HVQFN32 Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES YES	HVQFN24	Different pin map. SC16IS760 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS760 supports SPI speed up to 15 MH	YES	YES
TSSOP16 Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible  YES  YES  HVQFN24 Use SC16IS760 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  PES  YES  YES  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports  YES  YES  YES  YES  YES  YES  YES  YE	TSSOP16	Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. Software compatible	YES	YES
HVQFN24  Use SC16IS760IBS + external RS-232 drivers  YES  YES  HVQFN32  Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28  Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES  YES  YES	TSSOP24		YES	YES
HVQFN32 Different pin map. SC16IS762 does not have these features: ENIR and EN485 pins, 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28 Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES YES	TSSOP16	Different pin map. SC16IS740 does not have these features: 8X sampling rate, fractional baud rate generator. 5 MH max. Software compatible	YES	YES
TSSOP28  SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  TSSOP28  Different pin map. SC16IS762 does not have these features: 8X sampling rate, fractional baud rate generator. SC16IS762 supports SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible  YES  YES	HVQFN24	Use SC16IS760IBS + external RS-232 drivers	YES	YES
while XR20M1172 supports SPI speed of 5 MH max. Software compatible	HVQFN32	SPI speed up to 15 MH while XR20M1172 supports SPI speed of 5 MH max. Software compatible	YES	YES
HVQFN24 Use SC16IS762IBS + external RS-232 drivers YES YES	TSSOP28		YES	YES
	HVQFN24	Use SC16IS762IBS + external RS-232 drivers	YES	YES

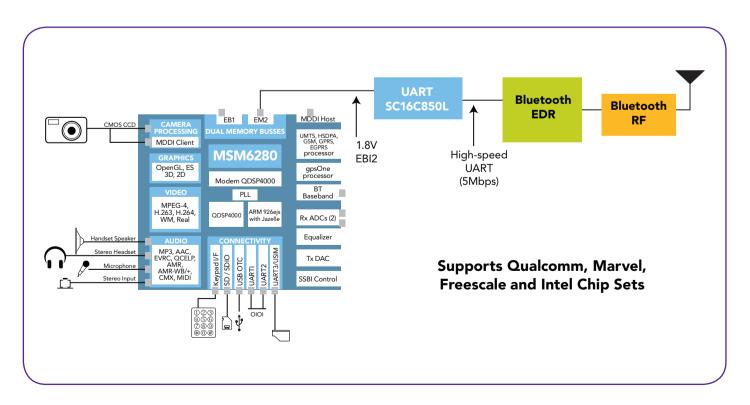
### TI 16C competitive cross-reference

TI Part Number	TI Description	TI Package	Cross Type	NXP Part Number	NXP Description	NXP Package	Hardware Change?	Software Change?	Commnent if not drop-in
					Single UART				
TL16C450	Single UART without FIFO	PLCC-44	Similar	SC16C550B	5 V, 3.3 V and 2.5 V UART with 16-byte FIFO	HVQFN-32, PLCC-44, LQFP-48, DIP-40	No	Yes	
TL16C451	Single UART without FIFO and with Parallel Port	PLCC-68	No Cross	N/A	N/A	N/A	N/A	N/A	
TL16C550C	Single UART with 16-Byte FIFO and Auto Flow Control	PLCC-44, LQFP-48, TQFP-48	Drop-in	SC16C550B	5 V, 3.3 V and 2.5 V UART with 16-byte FIFO	HVQFN-32, PLCC-44, LQFP-48, DIP-40	No	No	
TL16C550D	Asynchronous Communications Element with Auto Flow Control	BGA-24 µ*Jr, QFN-32, LQFP-48, TQFP-48	Drop-in	SC16C550B	5 V, 3.3 V and 2.5 V UART with 16-byte FIFO	HVQFN-32, PLCC-44, LQFP-48, DIP-40	No	No	
TL16C750	Single UART with 64-Byte FIFO, Auto Flow Control and Low Power Modes	PLCC-44, LQFP-64	Drop-in	SC16C750B	5 V, 3.3 V and 2.5 V UART with 64-byte FIFO	HVQFN-32, PLCC-44, LQFP-64	No	No	
TL16PC564B	Singl UART with 64-Byte FIFO, PCMCIA Interface	LQFP-100	No Cross	N/A	N/A	N/A	N/A	N/A	
					Dual UART				
TL16C2550	1.8 V to 5 V Dual UART with 16-Byte FIFO	QFN-32, TQFP-48, DIP-40	Similar	SC16C2550B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 16-byte FIFO	HVQFN-32, PLCC-44, LQFP-48, DIP-40	No	Yes	No Auto RTS/CTS, 32 pin package is not drop-in
TL16C2552	1.8 V to 5 V Dual UART with 16-Byte FIFO	PLCC-44	Similar	SC16C2552B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 16-byte FIFO	PLCC-44	No	Yes	No Auto RTS/CTS
TL16C2752	1.8 V to 5 V Dual UART with 64-Byte FIFO	PLCC-44	Similar	SC16C752B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 64-byte FIFO	LQFP-48, HVQFN-32	Yes	Yes	Does not support these registers: EMSR, FLVL, AFR, DREV, DVID, TRG, FC, FCTR
TL16C452	Dual UART without FIFO and with Parallel Port	PLCC-68	No Cross	N/A	N/A	N/A	N/A	N/A	
TL16C552	Dual UART with 16-Byte FIFO and Parallel Port	PLCC-68	No Cross	N/A	N/A	N/A	N/A	N/A	
TL16C552A	Dual UART with 16-Byte FIFO and Parallel Port	PLCC-68, LQFP-80	No Cross	N/A	N/A	N/A	N/A	N/A	
TL16C752B	Dual UART with 64-Byte FIFO	LQFP-48	Drop-in	SC16C752B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 64-byte FIFO	LQFP-48, HVQFN-32	No	No	
TL16C752B-EP	Enhanced Product, 3.3 V Dual UART with 64-Byte FIFO	LQFP-48	Drop-in	SC16C752B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 64-byte FIFO	LQFP-48	No	No	-40 to 85 while TI support -55 to 110
TL16C752C	Dual UART with 64-Byte FIFO	QFN-32, TQFP-48	Similar	SC16C752B	5 V, 3.3 V and 2.5 V Dual UART, 5-Mbit/s (max.), with 64-byte FIFO	LQFP-48, HVQFN-32	No	Yes	Does not support AFR register
TL16PIR552	Dual UART with 16-Byte FIFO, Selectable IR and 1384 Modes	QFP-80	No Cross	N/A	N/A	N/A	N/A	N/A	
TL28L92	3.3 V / 5 V Dual UART	QFP-44, QFN48	Drop-in	SC28L92	3.3 V / 5 V Dual UART	QFP44, HVQFN48	No	No	
					Quad UART				
TL16C554	Quadruple UART with 16-Byte FIFO	PLCC-68, LQFP-80	Drop-in	SC16C554B, SC16C554DB	5 V, 3.3 V and 2.5 V Quad UART, 5-Mbit/s (max.), with 16-byte FIFO	PLCC-68, LQFP-64, LQFP- 80, HVQFN-48	No	No	
TL16C554A	Quadruple UART with 16-Byte FIFO	PLCC-68, PQFP-64, TQFP-80	Drop-in	SC16C554B, SC16C554DB	5 V, 3.3 V and 2.5 V Quad UART, 5-Mbit/s (max.), with 16-byte FIFO	PLCC-68, LQFP-64, LQFP- 80, HVQFN-48	No	No	
TL16C754B	Quadruple UART with 64-Byte FIFO	PLCC-68, TQFP-80	Drop-in	SC16C754B	5 V, 3.3 V and 2.5 V Quad UART, 5-Mbit/s (max.), with 64-byte FIFO	PLCC-68, LQFP-64, LQFP-80	No	No	
TL16C754C	1.8 V to 5 V Quadruple UART with 64-Byte FIFO	LQFP-64, TQFP-64	Drop-in	SC16C754B	5 V, 3.3 V and 2.5 V Quad UART, 5-Mbit/s (max.), with 64-byte FIFO	PLCC-68, LQFP-64, LQFP-80	No	No	Does not support 1.8 V

### **Application notes (industrial)**

	SCC2681	SCC2681T	SCC68681	SCC2691	SCC2692	SCC68692	SCC2698B	SC26C92	SC28L91	SC28L92	SC28C94	SC28L194	SC28L198	SC28L202	SC28L201
AN405 SCN2681/SCN68681 and SCC2691 data communications	<b>•</b>	<b>•</b>	<b>•</b>	V.	<b>•</b>	<b>•</b>	•	•	<b>•</b>	<b>•</b>	<b>•</b>			<b>•</b>	¥.
AN410B SC2698B Octal Universal Asynchronous Receiver/Transmitter	•	•	•	•	•	•	V	•	•	•	•				
AN413 Using the Datacom product's on-chip oscillator	V.	V.	Ľ.	V.	V.	V.	V.	•	•	•	•				
AN414 SCC2692 differences from the SCN68681			•		V	•									
AN415 SCC68692 differences from the SCN68681			•		•	V.									
AN421 SCC2698A differences from the SCC2698B							V.								
AN462 Hardware and software verification procedure	•	•	•	•	•	•	•	•	•	•	<b>•</b>	<b>•</b>	•	•	•
AN4004 Electrostatic discharge protection	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10251 Automatic '485' turn-around	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10313 Reduce CPU overhead with Intelligence Interrupt Arbitration (I2A) feature												•	•	V.	<b>V</b>
AN10319 8051 microcontroller to UART serial interface evaluation board	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10320 Addressing migrations of SCN devices to more advanced technologies	V.	V	V	•	•	•	•	•	•	•	•	•	•	•	•
AN10339 UART serial interface through USB evaluation board	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10307 UART to Bluetooth interfacing	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10353 Application of UART in GPS navigation system	•	•	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10380 Ensure data integrity with real-time data error detection												•	•	Ľ.	∠
Article Extended baud rates for SCN2681/68681, SCC2691, SCC2692/68681 and SCC2698B	•	•	•	V.	V	•	V.	•							
Article Functional description of Philips arbitrating interrupt systems											V.	•	<b>•</b>	•	•

- = Applicable
- ∠ = Recommended



### **Application notes (16C)**

	SC16C550B	SC16C650B	SC16750B	SC162550B	SC16B2552B	SC16C652B	SC16C752B	SC16C554B	SC16C554DB	SC16C654B	SC16C654DB	SC16C754B	SC16IS740/ 741/750/760/ 752/762
AN10219 Using SC16C650B to implement an IrDA interface	•	Ľ				•				•	•		
AN10224 SC16C554B/SC16C654B ISA bus hardware interface example								K	•	K	•	•	
AN10249 SC16C752B/SC16C2550B ISA bus hardware interface example	•	•	•	V.	•	•	V						
AN10250 Using a Philips 16C UART to implement a simple RS-485 transmitter and receiver node	•	•	•	•	•	•	•	•	•	•	•	•	
AN10257 Differences between Philips SC16C devices and Philips low power SC16CxxxB devices	V	K	V.	Ľ	V	Ľ	V						
AN10307 UART to Bluetooth interfacing	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10312 Differences between Philips 4-channel SC16C devices and Philips low power SC16CxxxB devices								Ľ	V	Ľ	V.	Ľ	
AN10319 8051 microcontroller to UART serial interface evaluation board	•	•	<b>•</b>	•	•	•	•	•	<b>•</b>	•	<b>•</b>	•	
AN10333 SC16CXXXB baud rate deviation tolerance	•	•	•	•	•	•	•						•
AN10339  UART serial interface through USB evaluation board	•	•	•	•	•	•	•	•	•	•	•	•	
AN10353 Application of UART in GPS navigation system	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10366 HVQFN application information	•	•	•	•		•	•	•	•	•	•		
AN10386 Baud rate calculation for NXP 16C UARTs	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10608 XTAL1 clock and -IOW pulse synchronization	•	•	•	•	•	•	•	•	•	•	•	•	
AN10251 Automatic RS-485 turn-around	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10486 Automatic RS-485 address detection	•	•	•	•	•	•	•	•	•	•	•	•	•
AN10631 Possibility of erroneous transmitter interrupt In 16C 4-Channel UARTs								•	•	•	•	•	
AN10485 SDA pin connection in SPI mode													•
AN10571 Sleep programming for Bridge ICs	V	K	V	V.	V	V.	V	V.	V.	V.	V	V.	•
AN10587 Interface NXP Bridge ICs with NXP ARM controller													•
AN10417 SC16IS760/762 Fast IrDA mode													•

### **Application notes (Bridges)**

	SC18IS600	SC18IS601	SC18IS602B	SC18IS603	SC118IM700
AN10397 How to use the SC18IM700 to control any I <sup>2</sup> C-Bus device					•
AN10452 Interfacing NXP Bridge ICs with microcontroller	K	¥.	¥.	K	
AN10462 SPI Programming for NXP Bridge ICs	K	V.	V.	K	
AN10587 Interfacing NXP Bridge ICs with NXP ARM micro-controller	K	V.	V.	V	
AN10688 SC18IS602 Rev A Errata			•	•	

### Differences between NXP industrial UARTs\* and NXP SC16CxxxB devices

Feature	Industrial	SC16CxxxB
Supply voltage	3.3 and 5.0 V	2.5, 3.3 and 5.0 V
Temperature range	-40 to +85°C1	-40 to +85°C1
Channels	1, 2, 4, and 8	1, 2, and 4
Synchronous bus interface	Yes <sup>2</sup>	No
Independent transmit and receive baud rates	Yes	No
Maximum FIFO depth	Up to 256 bytes	64 bytes
Transmit and receive FIFOs	Yes3	Yes3
In-band (software) flow control (xon/xoff)	Yes	Yes
Out-of-band (hardware) flow control (RTS/CTS)	Yes	Yes
Multi-drop mode/RS485	Auto	Software required
Character recognition (also used for xon/xoff)	Yes	Yes
Bus cycle time (read strobe and read cycle delay)	40 ns – 125 ns	43 ns
Bus interface	Intel, Motorola, or both	Intel, Motorola, or both
Interrupt priority	Programmable	Fixed
Programmable interrupt vector format	Yes	No
IACKN and DACKN signal pins	Yes	No
Transmitter and receiver software reset	Yes	No
Independent transmitter and receiver enable/disable	Yes	No
Maximum baud rate	3.125 Mbps	5 Mbps
Receiver Watchdog timer	Yes	No
Programmable data format	5 to 8 data bits	5 to 8 data bits
Parity format	Odd, even, forced, none	Odd, even, forced, none
Number of stop bits	1, 1-1/2, or 2	1, 1-1/2, or 2
Baud rate selection	Programmable	Programmable
Parity, framing, and overrun detection	Yes	Yes
Line-break detection and generation	Yes	Yes
Automatic echo of received character	Yes	No
Local loop back	Yes	Yes
Remote loop back	Yes	No
Loop back error check	Yes	No
Programmable I/O port pins	Yes	No
Infrared IrDA interface	No	Yes
Change-of-state detection	CD, RI, CTS, DSR, and all I/O pins	CD, RI, CTS, DSR
Power-down mode	Yes <sup>4</sup>	Yes <sup>4</sup>
Clock frequency using on-chip oscillator and external crystal	Up to 16.2 MHz	Up to 24 MHz
TTL input levels	Yes	Yes
Software	Similar structures but different low-level rou	utines
Receiver time-out mode	Yes	Yes

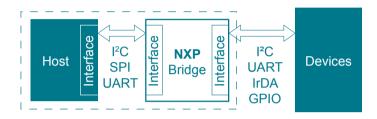
<sup>\*</sup> Note: UART products falling into industrial category: SCCxxx, SC28xxx, SC26xxx, SC28Lxxx

 $<sup>^1</sup>$  Industrial temperature at commercial price  $^2$  Synchronous use requires a clock from host  $^3$  FIFO depth varies depending on UART  $^4$  Clock is shut off but register contents remain <sup>3</sup> FIFO depth varies depending on UART

### **NXP** Bridges

NXP bridge ICs are the new generation of serial interface solutions for managing host-to-device communications among wide variety of serial bus interfaces such as I<sup>2</sup>C, SPI, and UART. Our bridges allow you to simply connect devices that use a different serial bus than your current system. These products operate at low voltages, consume little power, and come in ultra-small packaging. They are ideal for battery-operated applications. In addition, our bridges improve overall system performance by reducing software overhead while increasing design flexibility.

A host processor may not be able to communicate with devices using multiple I<sup>2</sup>C, SPI, UART, IrDA, and GPIO interfaces. For many advanced applications, multiple bus ports are required. Without using our bridges, host processing and system complexity can be overwhelming. Our bridges free up processor resources for code and data memory by handling the interfacing directly. Our bridge products are optimized for high speed data throughput.



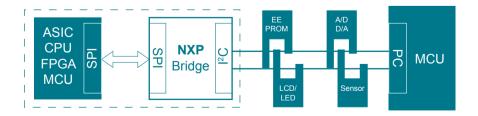
Our products seamlessly bridge hosts to serial and wireless devices with minimal wire interconnection. Our bridges enable you to overcome limitations of your system's host while adding design flexibility and reducing complexity to implement other bus interfaces.

### SPI slave to I<sup>2</sup>C master/GPIO bridges – SC18IS600

NXP bridge solutions include products that provide seamless protocol convergence between the two widely-used, bus-shared architectures: SPI and I<sup>2</sup>C. Our bridges allow hosts having an SPI bus to easily communicate with I<sup>2</sup>C-bus devices such as LCD displays, temperature/voltage sensors, and EEPROM data storage. Our bridges' GPIOs further allow you to expand your system for detecting push buttons/keypads and controlling LEDs and fans.

The bridge interface to the SPI bus is a slave that uses four wires (2 data and 2 control signals) and operates at speeds as high as 1.2 Mbps. The bridge interface to the I<sup>2</sup>C bus is a master that uses two wires (SCL and SDA) and supports speeds up to 400 KHz. The bridge GPIOs are configurable.

These bridges operate from 2.4 V to 3.6 V and come in TSSOP packages and feature a power-down mode. They can be used in industrial applications with a temperature range of -40 to +85 °C.



An SPI slave to I<sup>2</sup>C-bus master bridge allows a host with SPI-bus capability to transparently communicate to an I<sup>2</sup>C-bus. Our bridge's I<sup>2</sup>C bus controller has multi-master capability. This allows it to share the bus with other I<sup>2</sup>C masters such as a microcontroller.

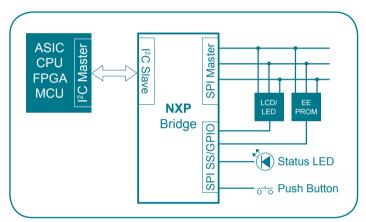
### I<sup>2</sup>C slave to SPI master/GPIO bridges – SC18IS602B

These bridges connect to an I<sup>2</sup>C master port of a microcontroller or other host processor and allow you to communicate with SPI slave peripherals. The bridges have an I<sup>2</sup>C slave interface on one side and an SPI master interface on the other. The bridges can also be used to provide additional GPIO to an I<sup>2</sup>C master by utilizing unused SPI slave select outputs.

Each high-speed serial bridge allows you to add up to 4 SPI slave devices making them ideal for host processors without native SPI capabilities. The bridges have 8 selectable I<sup>2</sup>C addresses allowing you to further expand the host processor's SPI capabilities by using multiple bridges. The multiple I<sup>2</sup>C addresses also help avoid conflict with other I<sup>2</sup>C devices.

The SPI master interface of our bridges support SPI speeds up to 1.8 Mbps using an internal oscillator or up to 4 Mbps when used with an external oscillator. The I<sup>2</sup>C slave interface of our bridges supports speeds up to 400 KHz. Deep data buffers between the I<sup>2</sup>C and SPI interfaces enhance translation performance efficiency.

Our I<sup>2</sup>C slave to SPI master/GPIO bridges operate from 2.4 V to 3.6 V and feature a low-power mode. They are available in TSSOP packaging.



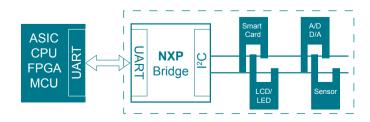
I<sup>2</sup>C slave to SPI master bridging allows a host with an I<sup>2</sup>C interface to transparently communicate with devices attached to the SPI-bus. This allows you to retain proprietary or specialized SPI peripherals such as an LCD display or an EEPROM in your I<sup>2</sup>C-based design.

### UART to I<sup>2</sup>C master/GPIO bridges - SC18IM700

NXP Semiconductors offers easy-to-use bridge solutions that enable long distance communication with remote I<sup>2</sup>C or GPIO devices via a simple RS232 connection. By using our UART to I<sup>2</sup>C master/GPIO bridges, you can easily detect remote push button/keypad presses, sense environmental conditions in distant systems, control LEDs/displays, and operate fans.

The bridges' I<sup>2</sup>C master interface controls the I<sup>2</sup>C bus without needing a remote host processor. For more advanced remote systems, the I<sup>2</sup>C interface is also multi-master capable. The I<sup>2</sup>C interface supports speeds up to 400 KHz. The bridges' UART interface offers high-speed transfer rates up to 460.8 Kbps and has a sleep/power-down mode with wake-up pin.

The bridges operate from 2.3 V to 3.6 V and come in TSSOP packages. They can be used in industrial applications with a temperature range of -40 to +85 °C.



The UART to I<sup>2</sup>C-bus master bridge functionality allows a host with RS232 capability to communicate with remote I<sup>2</sup>C devices. Remote I<sup>2</sup>C devices can include temperature sensors, LCD displays, A/D converters, and smart card readers.

### NXP Bridge – Demo Board Kits

I <sup>2</sup> C/SPI slave to UART	UART to I <sup>2</sup> C master
SC16IS7xx	SC18IM700
Kits include  ▶ Sample code: RS232, RS485, and IrDA ▶ User Manual	Kits include  ▶ Sample code: RS232 and NXP I <sup>2</sup> C devices ▶ User Manual
<ul> <li>Key Benefit</li> <li>▶ Easy interface to I<sup>2</sup>C/SPI host and IrDA, RS232/RS485, and GPIO devices.</li> <li>▶ Selectable I<sup>2</sup>C or SPI-bus interface</li> </ul>	<ul> <li>Key Benefit</li> <li>▶ Easy interface to UART host and various I<sup>2</sup>C and GPIO devices.</li> <li>▶ On-board I<sup>2</sup>C EEPROM and I<sup>2</sup>C LED Dimmer</li> </ul>
OM6270 – SC16IS750 OM6273 – SC16IS752	OM6272

SPI to I <sup>2</sup> C master	I <sup>2</sup> C to SPI master
SC18IS600	SC18IS602
Kits include  ▶ Sample code: SPI and NXP I <sup>2</sup> C devices ▶ User Manual	Kits include  ▶ Sample code: I <sup>2</sup> C and NXP SPI devices ▶ User Manual
<ul> <li>Key Benefit</li> <li>▶ Easy interface to SPI host and various I<sup>2</sup>C and GPIO devices.</li> <li>▶ On-board I<sup>2</sup>C EEPROM and I<sup>2</sup>C LED Dimmer</li> </ul>	<ul> <li>Key Benefit</li> <li>▶ Easy interface to I<sup>2</sup>C host and SPI and GPIO devices.</li> <li>▶ Up to 4 SPI chip selects</li> </ul>
OM6271	OM6274

Notes		

Notes			

# NXP UARTs in HVQFN and TFBGA -

an ideal solution for mobile applications

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